

RJK1051DPB

100V, 15A, 39m Ω max. Silicon N Channel Power MOS FET Power Switching

Features

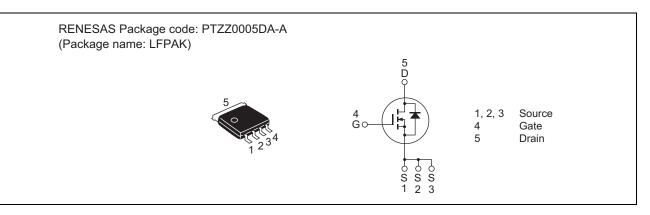
- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting

• Low on-resistance

 $R_{DS(on)}\!=30~m\Omega$ typ. (at $V_{GS}\!=10~V)$

- Pb-free
- Halogen-free

Outline



Application

• Switching Mode Power Supply

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit	
Drain to source voltage	V _{DSS}	100	V	
Gate to source voltage	V _{GSS}	±20	V	
Drain current	Ι _D	15	А	
Drain peak current	Note1 I _{D(pulse)}	60	А	
Body-drain diode reverse drain current	I _{DR}	15	А	
Avalanche current	I _{AP} Note 2	7.5	А	
Avalanche energy	E _{AS} Note 2	5.6	mJ	
Channel dissipation	Pch Note3	45	W	
Channel to Case Thermal Resistance	θch-C	2.78	°C/W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Notes: 1. $PW \le 10 \ \mu s$, duty cycle $\le 1\%$

2. Value at Tch = 25°C, Rg \geq 50 Ω

3. Tc = 25°C

This product is for the low voltage drive (≤ 10 V).

If the driving voltage is over 10 V under normal conditions, please use the product for high gate to source cutoff voltage $(V_{GS(off)})$ which characteristics has been improved.



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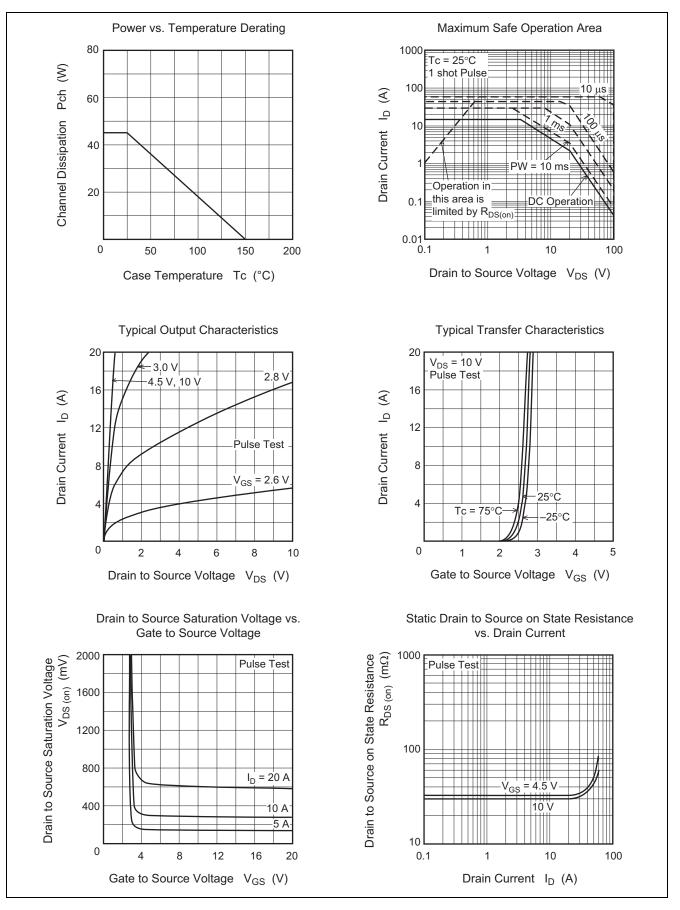
Electrical Characteristics

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR)DSS}	100	—		V	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$
Zero gate voltage drain current	I _{DSS}	_	—	10	μΑ	$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$
Gate to source cutoff voltage	V _{GS(off)}	1.2	—	2.5	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	30	39	mΩ	$I_D = 7.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R _{DS(on)}	_	33	46	mΩ	$I_D = 7.5 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y _{fs}	_	30	_	S	$I_D = 7.5 \text{ A}, V_{DS} = 10 \text{ V}^{Note4}$
Input capacitance	Ciss	_	2060	_	pF	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0 \text{ V},$ f = 1 MHz
Output capacitance	Coss	_	180	_	pF	
Reverse transfer capacitance	Crss	_	70	_	pF	
Gate Resistance	Rg	_	0.7	_	Ω	
Total gate charge	Qg	—	15	_	nC	$V_{DD} = 10 \text{ V}, V_{GS} = 4.5 \text{ V},$ $I_{D} = 15 \text{ A}$
Gate to source charge	Qgs	—	6.6	_	nC	
Gate to drain charge	Qgd	—	4.6	_	nC	
Turn-on delay time	t _{d(on)}	—	8.4	_	ns	
Rise time	tr	—	4.0	_	ns	
Turn-off delay time	t _{d(off)}	_	42		ns	
Fall time	t _f	_	4.5	_	ns	
Body-drain diode forward voltage	V _{DF}	_	0.83	1.1	V	$I_F = 15 \text{ A}, V_{GS} = 0 \text{ V}^{Note4}$
Body–drain diode reverse recovery time	t _{rr}	_	40	_	ns	$I_F = 15 \text{ A}, V_{GS} = 0 \text{ V}$ $di_F/dt = 100 \text{ A}/\mu \text{s}$

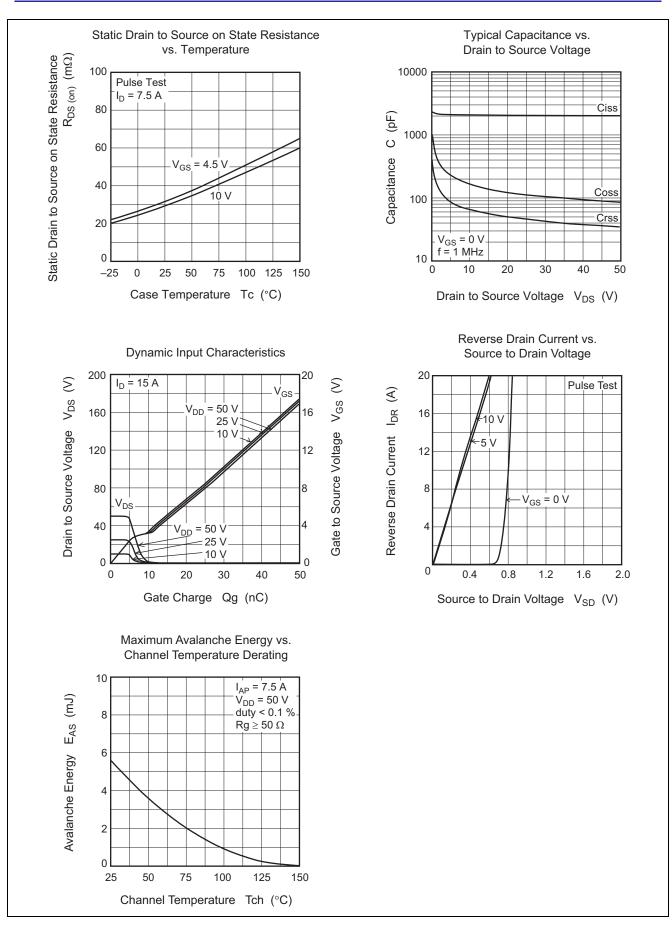
Notes: 4. Pulse test

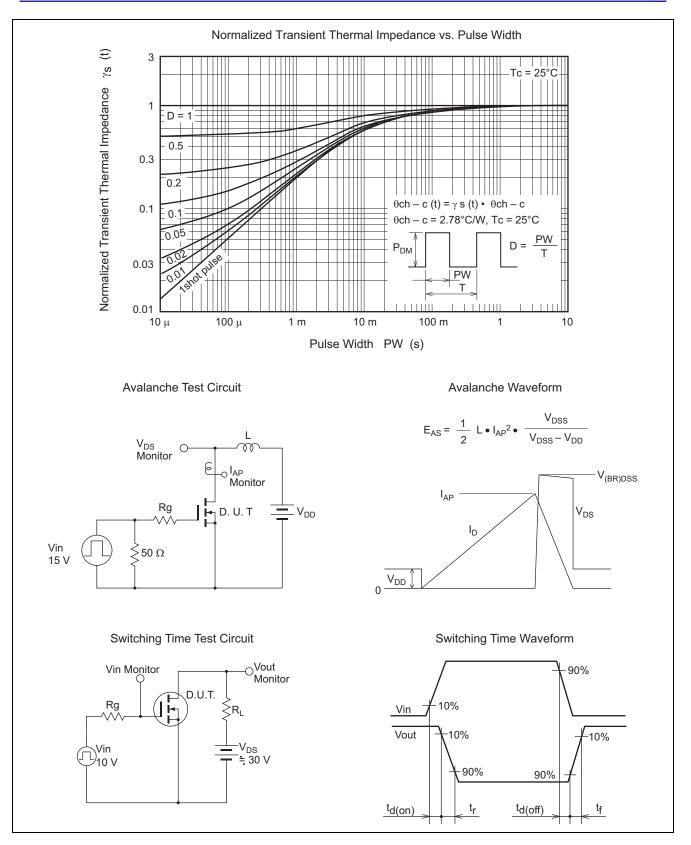


Main Characteristics

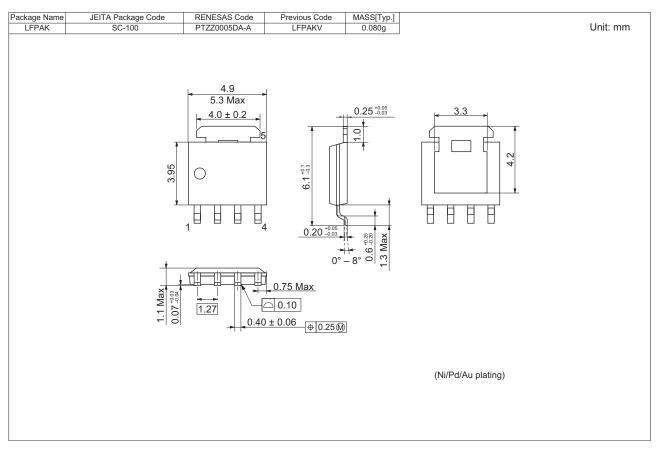








Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJK1051DPB-00-J5	2500 pcs	Taping



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